

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: **Alon et al**

Serial No.: 10/091,934

Group Art Unit: 2825

Filed: March 06, 2002

Examiner: Naum B. Levin

Title: AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED
CIRCUIT DESIGN

Honorable Commissioner for Patents

P.O. 1450

Alexandria, Virginia 22313-1450

Amendment

Sir:

This amendment is filed in response to the Office Action dated May 11, 2007, issued by the USPTO in connection with the above-identified patent application. A response to the May 11, 2007 Office Action was due August 11, 2007. Applicants are concurrently filing a Petition for a One-Month Extension of Time, including the required fee. Therefore, a response is due September 11, 2007. Accordingly, this Amendment is being timely filed.

Kindly amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.